Name	**Dr.B.RAJENDRA NAIK**
Designation	**Associate Professor**
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Mr.B.Rajendra Naik has obtained his Bachelor of Technology in Electronics and Communications Engineering from Nagarjuna University in 2000 and Master of Engineering in Digital Systems Engineering from Osmania University, Hyderabad. Doctorate Degree in Electronics and Communications Engineering in the area of VLSI, Osamnia University, Hyderabad. Currently he is working as Associate Professor in Dept of Electronics and Communication Engineering, College of Engineering, Osmania University, Hyderabad. He has published several Papers in National and International Conferences and Journals. He guided many BE and ME students in projects. He worked as visiting Researcher in Tokyo Metro Politan University, Tokyo, JAPAN in 2010. Presently Guiding PhD Students in the Area of VLSI & Image Pracessing.

Professional Interest:

A commitment to education through teaching in broad areas of:

- VLSI Design
- Electronic Devices and Circuits
- Switching Theory and Logic Design
- Image Processing
- Digital Electronics
- Digital Design and PLDS
- Integrated Circuit Applications
- Digital IC Design

Education:

- **Ph.D**, Signal Integrity performance improvement in VLSI circuits, Osmania University, Hyderabad,Telangana State
- **M.E**, Digital Systems Engineering, College of Engineering, Osmania University, Hyderabad, Telangana State
- **B.Tech**, Electronics and Communication Engineering, Nagarjuna University, Andhra Pradesh

Outreach Activities:

- Secretary OUCE Alumni Association
- Member of IACSIT

Research Interests:

- VLSI
- Image Processing

Countries Visited:

- Japan
- Malaysia
- Australia
- United Kingdom

Awards:

1. Awarded Sir Visvesvaraya **Young Faculty Research Fellowship** from Dept of Electronics & Information Technology (DeitY) Govt of India in 2016